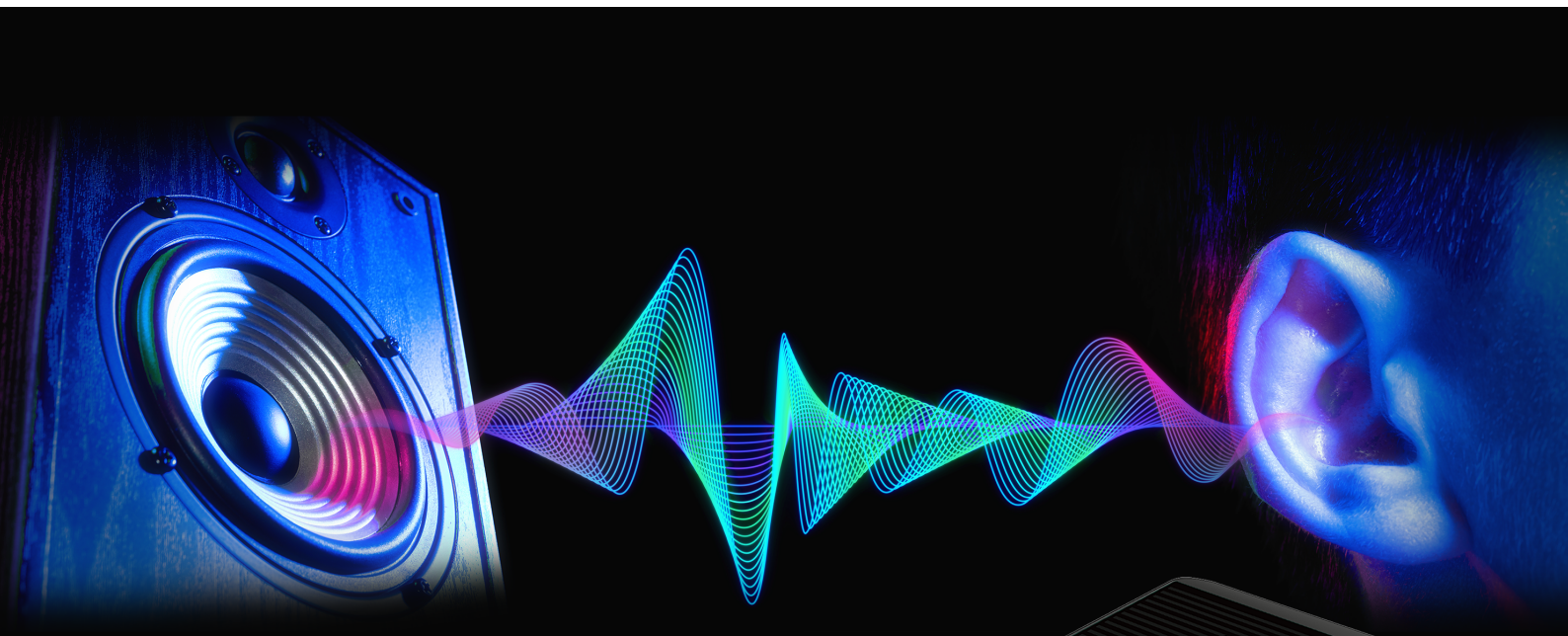
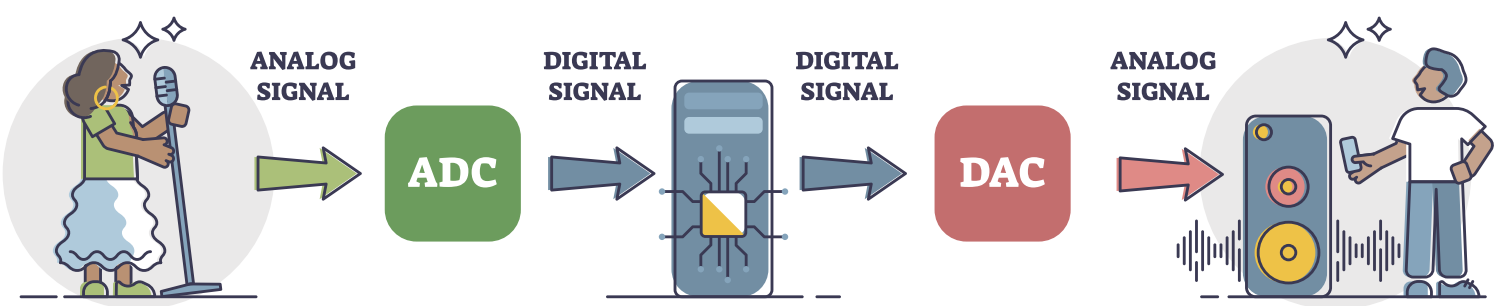


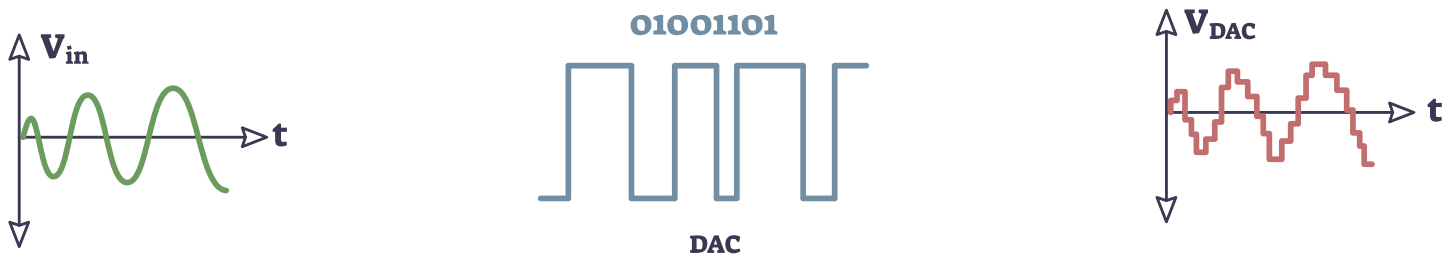
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LAP-C Pro I2S bus analysis

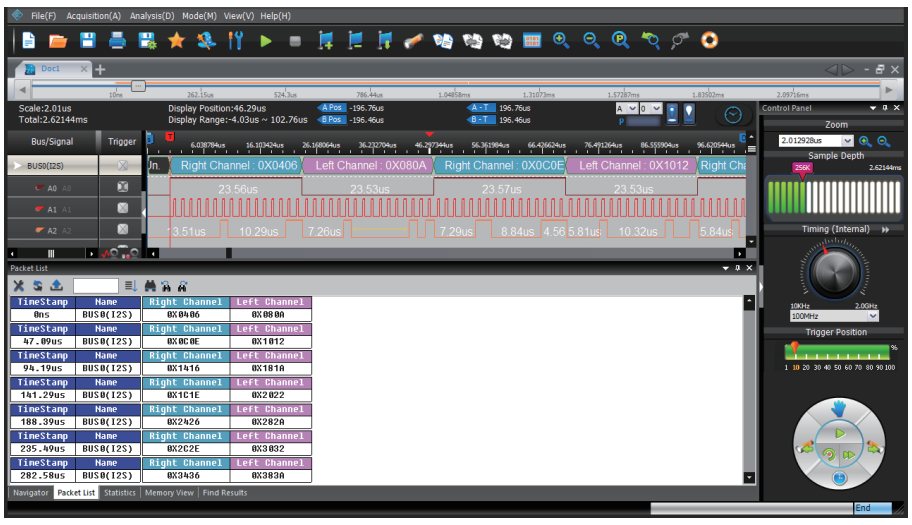


Digital audio has many advantages for systems that need to do a lot with the audio signal; so the market for digital audio ICs has grown in many new and unexpected sectors. I2S is an interface standard designed to transmit digital audio data. It defines the hardware interface specification and the format of digital audio data, typically used to transfer PCM audio data from a CD to a DAC of a CD player.





I2S transmits 2 groups (left and right channels) data in a sequential manner, and consists of 3 transmission lines: clock line (SCLK), selection line (LRCK), and data line (SDATA). Three different data formats have been developed, namely Philips standard, left-justified standard, and right-justified standard.



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LAP-C Pro
Demo decode I2S
YouTube

Audio developers have traditionally tested and debugged I2S and I2C manually, by visually counting bits. This method is tedious and prone to errors. Audio ICs often support other protocols to give developers more flexibility，so having an I2S,I2C and SPI signal analysis toolkit is essential for any developer working in consumer electronics. Zeroplus Logic Analyzer provides a variety of bus debugging tools, which can help developers identify protocol packets and debug complex hardware timing problems, check the data in the protocol, analyze protocol activities or error audio, and help your product fully recover the voice signal.